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Patent/TM/App/Serial # 10/684,727

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ATTENTION ATTENTION ATTENTION

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Atty. Dkt. No. 039153-0667

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qi XIANG

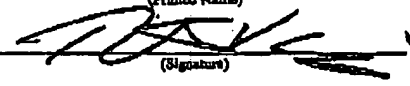
Title: STRAINED SILICON MOSFET
HAVING IMPROVED CARRIER
MOBILITY, STRAINED SILICON
CMOS DEVICE, AND METHODS
OF THEIR FORMATION

Appl. No.: 10/684,727

Filing Date: 10/14/2003

Examiner: Trinh, Michael M.

Art Unit: 2822

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
EV 445623971 US (Express Mail Label Number)	May 4, 2005 (Date of Deposit)
Rufie Valiejo (Printed Name)	
 (Signature)	

REQUEST FOR REFUND

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby requests a refund of \$300.00 for payment of the publication fee that was erroneously charged in the Notice of Allowance and Fee(s) Due. The above-identified application was filed with a Request and Certificate Under 35 U.S.C. 122(b)(B)(i) on October 13, 2003. The application was not published and so no publication fee was due.

Attached is a copy of check no. 881527 in the amount of \$1,700.00 for payment of the Issue Fee (\$1,400) and Publication Fee (\$300) dated December 21, 2004, and a copy of a date-stamped postcard showing receipt of the check by the Patent Office. Applicant respectfully requests that a credit of \$300.00 be made to Deposit Account No. 50-0872.

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Atty. Dkt. No. 039153-0667

In view of the foregoing, it is believed that no fee is required for filing this petition, but should a fee be required, the PTO is authorized to charge an appropriate fee for the filing of this petition to Deposit Account No. 50-0872.

Respectfully submitted,

Date 4 May 2005

By Ronald Coslick

FOLEY & LARDNER LLP
Customer Number: 23392
Telephone: (310) 975-7964
Facsimile: (310) 557-8475

Ronald Coslick
Attorney for Applicant
Registration No. 36,489

Repln. Ref: 09/15/2005 RCLEMONS 0016055900
DAH: 500872 Name/Number: 10684727
FC: 9204 \$300.00 CR

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Date: Dec 21, 200

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U.S. Express Mail Label No. EV 445626008 US Aty. Dkt. 039153-0667
Receipt is hereby acknowledged in the U.S. Patent and Trademark Office:

Applicant: QI XIANG
Serial No.: 10/684,727
Filing Date: 10/14/2003
Title: STRAINED SILICON MOSFET HAVING IMPROVED
CARRIER MOBILITY, STRAINED SILICON CMOS DEVICE, AND
METHODS OF THEIR FORMATION

- ☒ Transmittal of Issue Fee (1 page)
- ☒ Part-B Issue Fee Transmittal (PTOL-\$5) (1 page)
- ☒ "Fee Address" Indication Form (1 page)
- ☒ Transmittal for Formal Drawings (1 page)
- ☒ Figures 1 and 2 (1 sheet)
- ☒ Check for \$1,790.00 for payment of the Issue Fee and Publication Fee.

Date Due: 12/27/2004
Date Mailed: 12/21/2004
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FOLEY & LARDNER LLP
ATTORNEYS AT LAW
2029 CENTURY PARK EAST, SUITE 9500
LOS ANGELES, CA 90067-3021
TELEPHONE: 310.277.2223
FACSIMILE: 310.557.8476
WWW.FOLEY.COM

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Total # of Pages 7 (Including this page)

TO:	PHONE #:	FAX #:
Refund Department, U.S. Patent & Trademark Office		(571) 273-6500

From: Ronald Coslick

Date: July 19, 2005

Client/Matter No: 039153-0667

User ID No: 0216

MESSAGE:

Re: U.S. Patent Application No.: 10/684,727
Filing Date: 10/14/2003
Title: STRAINED SILICON MOSFET HAVING IMPROVED CARRIER
MOBILITY, STRAINED SILICON CMOS DEVICE, AND METHODS OF
THEIR FORMATION
Inventor(s): QI XIANG
Our Ref: 039153-0667

Per my conversation today with your refund department, please find attached a resubmission of the request for refund filed on May 4, 2005.

I hereby certify that copies of the following paper(s) and/or fee along with any attachments referred to or identified as being attached or enclosed are being facsimile transmitted to the U.S. Patent and Trademark Office (Fax No. (571) 273-6500) on the date of shown above.

1. Copy of postcard dated May 4, 2005 and Copy of Express Mail Receipt
2. Copy of Request for Refund
3. Copy of Check # 881527 and Copy of postcard

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Operator:	Time Sent:	Return Original To: Ruth M. Vallejo
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U.S. Express Mail No. EV 445623871 US Atty. Dkt. 039153-0667
Receipt is hereby acknowledged in the U.S. Patent and Trademark Office:

Applicant: Qi Xiang
Serial No.: 10/684,727
Filing Date: 10/14/2003
Title: STRAINED SILICON MOSFET HAVING IMPROVED
CARRIER MOBILITY, STRAINED SILICON CMOS
DEVICE, AND METHODS OF THEIR FORMATION

- ☒ Request for Refund (2 pages)
- ☒ Copy of Check No. 881527 in the amount of \$1,700 (1 page)
- ☒ Copy of date-stamped postcard (1 page)

Date Mailed: May 4, 2005



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